

214019US-2



IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF: :

EIJI SAKAGAMI : GROUP ART UNIT: 2814

SERIAL NO.: 09/955,076 :

FILED: SEPTEMBER 19, 2001 : EXAMINER: WEISS, HOWARD

FOR: NONVOLATILE SEMICONDUCTOR  
MEMORY AND METHOD OF FABRICATING THE SAME

**RESPONSE TO RESTRICTION REQUIREMENT**

ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

SIR:

In response to the Election Requirement dated April 15, 2002, Applicants elect without traverse Group I, Claims 1-6 drawn to a semiconductor device. Applicants make this election based on the understanding that Applicants are not prejudiced against filing one or more divisional applications that cover the non-elected claims.

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2002

Accordingly, it is respectfully requested that an examination on the merits of Claims 1-6 be conducted.

Respectfully submitted,

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ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231



RE: U.S. Application  
Serial No: 09/955,076  
Filed: SEPTEMBER 19, 2001  
Group: 2814  
Inventor: EIJI SAKAGAMI  
For: NONVOLATILE SEMICONDUCTOR  
MEMORY AND METHOD OF  
FABRICATING THE SAME

SIR:

Attached hereto for filing are the following papers:

**RESPONSE TO RESTRICTION REQUIREMENT**

Our check in the amount of \$-0-- is attached covering any required fees. In the event that any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 CFR 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate of this sheet is enclosed.

Respectfully submitted,

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